

ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention

STRUCTURE AND METHOD OF MAKING A BIPOLAR TRANSISTOR HAVING REDUCED COLLECTOR-BASED CAPACITANCE

Submission Type :

Utility Patent Filing

Application Number:

10/708860



EFS ID:

58044

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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IGEN77	Filename= N/A BusinessRule= The access code is not included which allows for automated processing to the deposit account. #Supporting Msg:Either <us-access-code> not found in fee-xml OR actual access code not provided in the fuction call.

First Named Applicant:

Hiroyuki Akatsu

Attorney Docket Number:

FIS920030415US1

Timestamp:

2004-03-29 15:10:07 EDT

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us

File Listing:

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